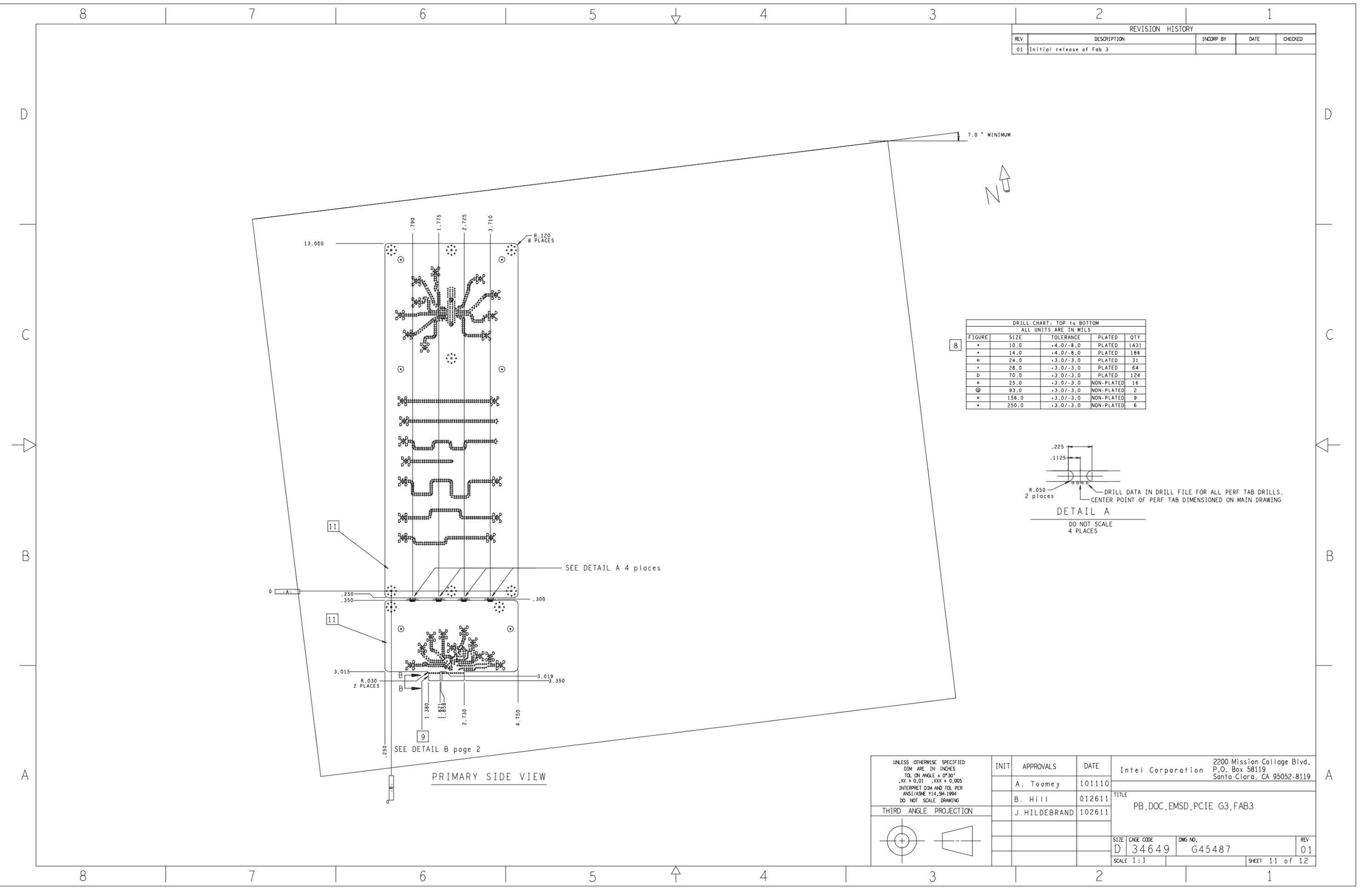
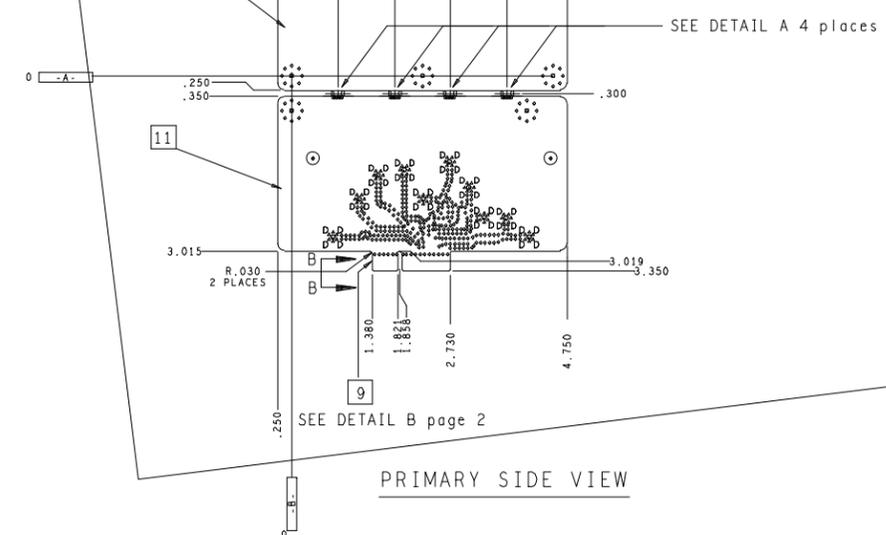
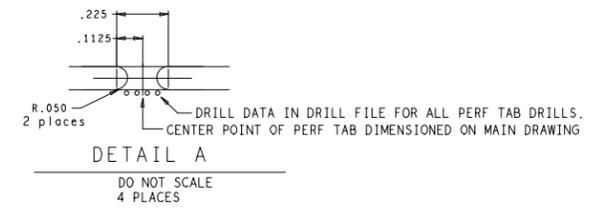


REVISION HISTORY			
REV	DESCRIPTION	INCRP BY	CHECKED
01	Initial release of Fab 3		



8

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
•	10.0	+4.0/-8.0	PLATED	1431
•	14.0	+4.0/-8.0	PLATED	186
•	24.0	+3.0/-3.0	PLATED	31
•	28.0	+3.0/-3.0	PLATED	64
•	70.0	+3.0/-3.0	PLATED	124
•	25.0	+3.0/-3.0	NON-PLATED	16
•	93.0	+3.0/-3.0	NON-PLATED	2
•	156.0	+3.0/-3.0	NON-PLATED	9
•	250.0	+3.0/-3.0	NON-PLATED	6



UNLESS OTHERWISE SPECIFIED DIM ARE IN INCHES TOL ON ANGLE ± 0°30' .XX ± 0.01 .XXX ± 0.005 INTERPRET DIM AND TOL PER ANSI/ASME Y14.5M-1994 DO NOT SCALE DRAWING	INIT	APPROVALS	DATE	Intel Corporation 2200 Mission Collage Blvd. P.O. Box 58119 Santa Clara, CA 95052-8119
		A. Toomey	101110	
		B. Hill	012611	
THIRD ANGLE PROJECTION		J. HILDEBRAND	102611	TITLE
				PB, DOC, EMSD, PCIE G3, FAB3
	SIZE	CAGE CODE	DWG NO.	REV
	D	34649	G45487	01
	SCALE 1:1			SHEET 11 of 12

