

INTEL CONFIDENTIAL

REVISION HISTORY

ZONE	REV	DESCRIPTION	DATE	APPROVED

HILLE GREEN FAB 3

CONNECTOR

PCIE GEN 3 TEST BOARD FAB 3

REVISED 10/07/2011

BASE BOARD

G45494-301 PBA,MB,PCIE GEN3 BB,FAB3
G45493-003 PB,EMSD,PCIE GEN3 BB,FAB3
G45487 PB,DOC,EMSD,PCIE G3,FAB3
G47418 PB, SCHEMATIC, EMSD, PCIE G3, FAB 3

RISER CARD

G45492-301 PBA,RC,PCIE GEN3 RC,PCIE G3 CEB RC,FAB3
G45491-003 PB,EMSD,PCIE G3 CEB RC,FAB3 (NEW)
G45487 PB,DOC,EMSD,PCIE G3,FAB3 (NEW)
G47418 PB, SCHEMATIC, EMSD, PCIE G3, FAB 3

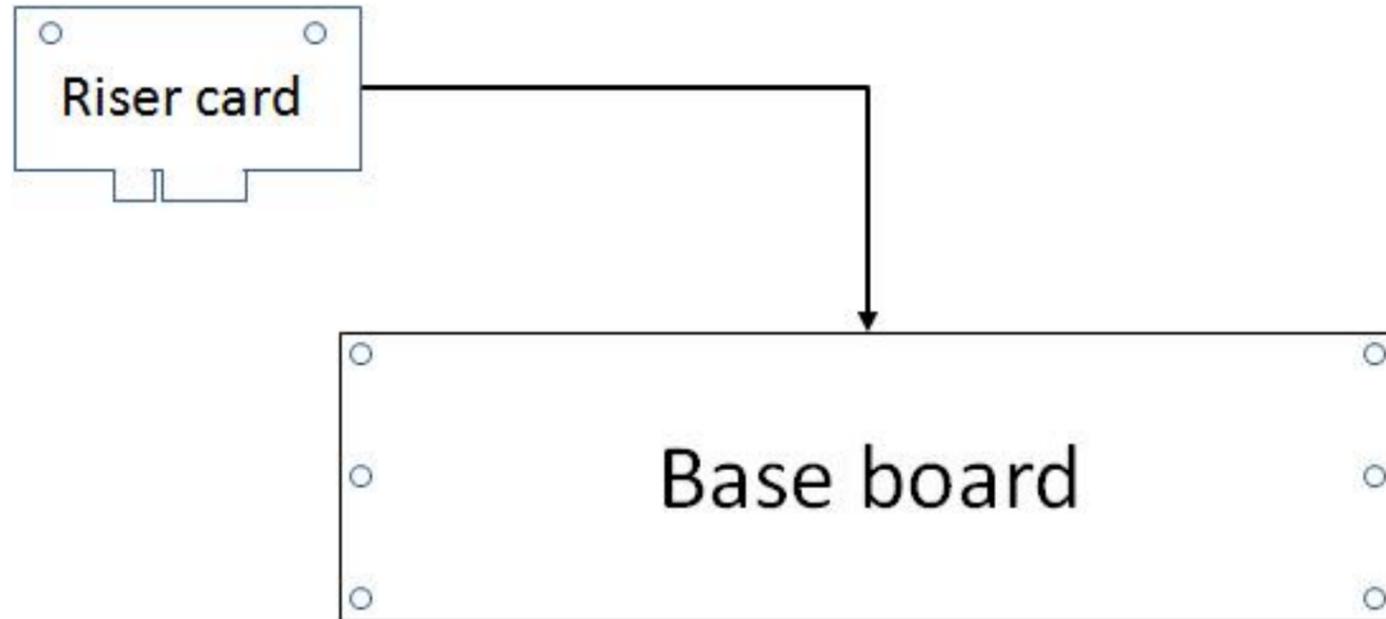
Fri Oct 07 16:48:40 2011

DESIGNED BY	DATE	Intel Corporation			2200 Mission Collage Blvd.
DRAWN BY	DATE	HILLE_GREEN			P.O. BOX 58119
CHECKED BY	DATE	HILLE_GREEN			Santa Clara, CA 95052-8119
APPROVED BY	DATE	HILLE_GREEN			
DEPARTMENT		SIZE	CODE	DOCUMENT NUMBER	REV
ESE		B	34649	G47418	0.1
SCALE: NONE		DO NOT SCALE DRAWING		SHEET 1 OF 6	

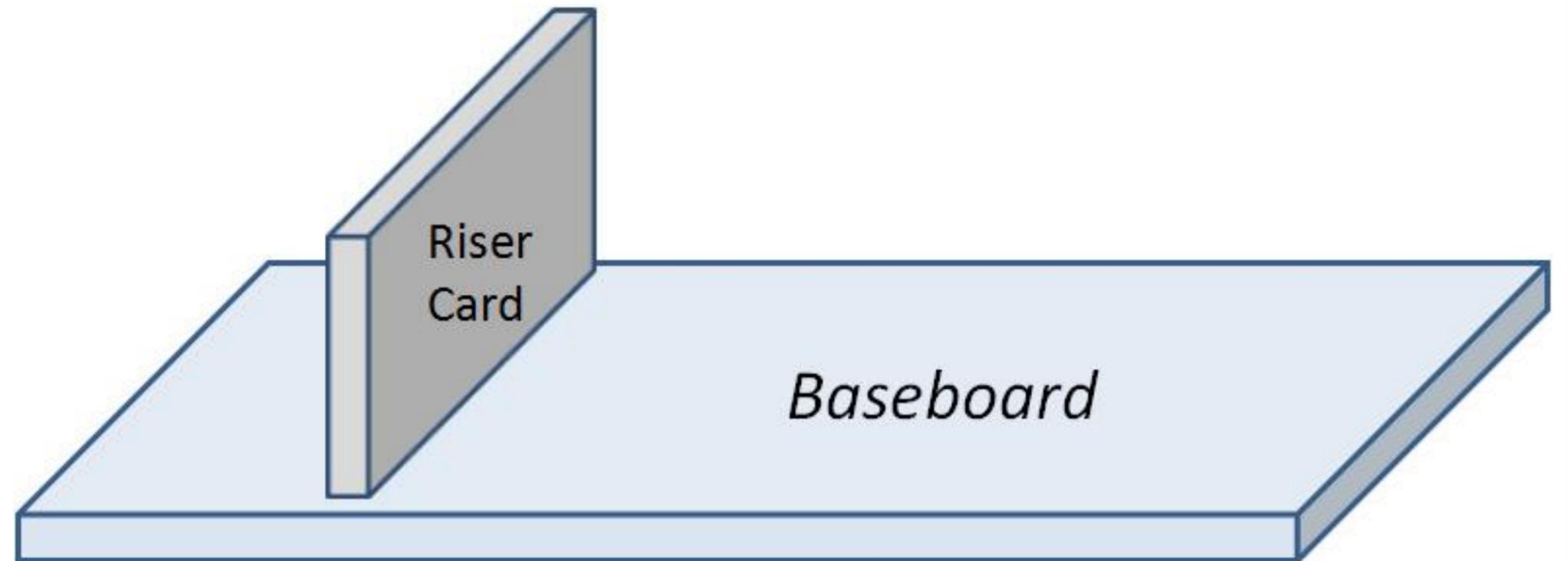
INTEL CONFIDENTIAL

DRAWING
TITLE=PCIE_GEN2_CONN
ABBREV=PCIE_GEN2_CONN
LAST_MODIFIED=Fri Oct 07 16:48:40 2011

RISER CARD & BASE BOARD
BLOCK DIAGRAM
VIEW 1



RISER CARD & BASE BOARD
BLOCK DIAGRAM
VIEW 2



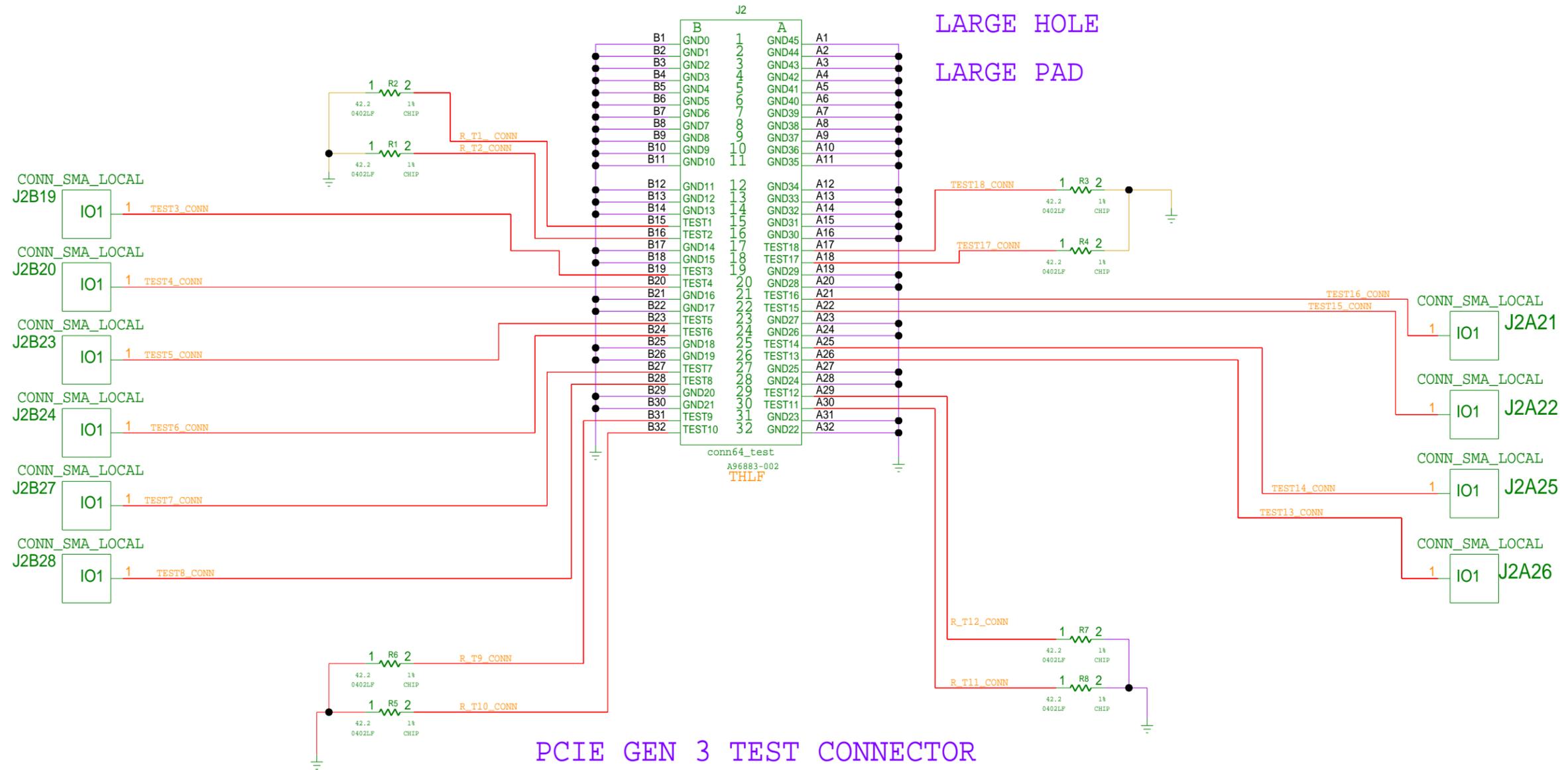
DEPARTMENT	Intel Corporation	SIZE	CODE	DOCUMENT NUMBER	REV
ESE	2200 Mission College Blvd. P.O. BOX 58119 Santa Clara, CA 95052-8119	B	34649	G47418	0.1
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BASE BOARD

SMALL ANTI-PAD

LARGE HOLE

LARGE PAD

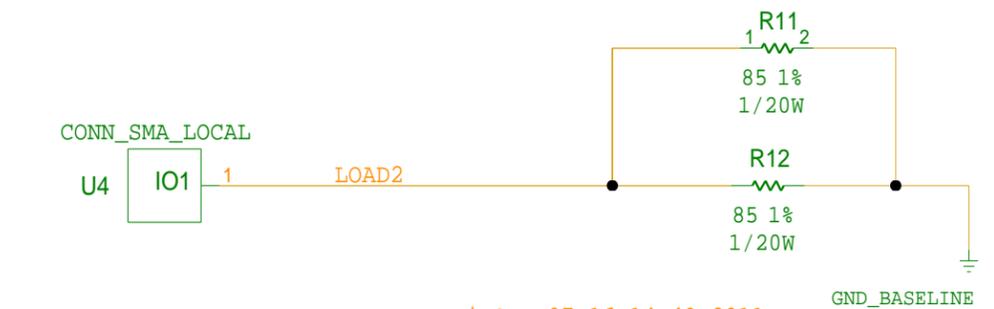
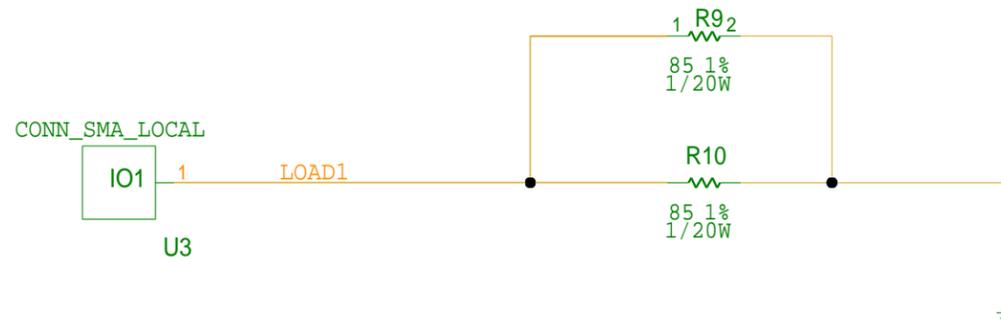
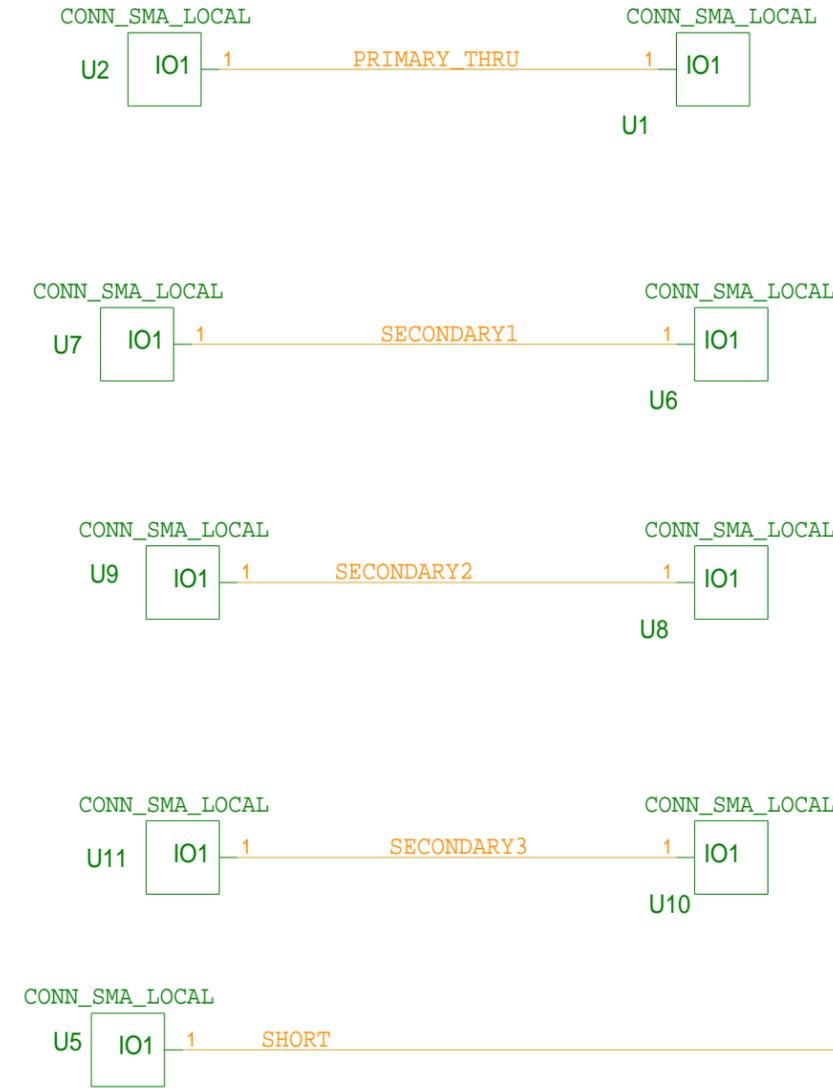
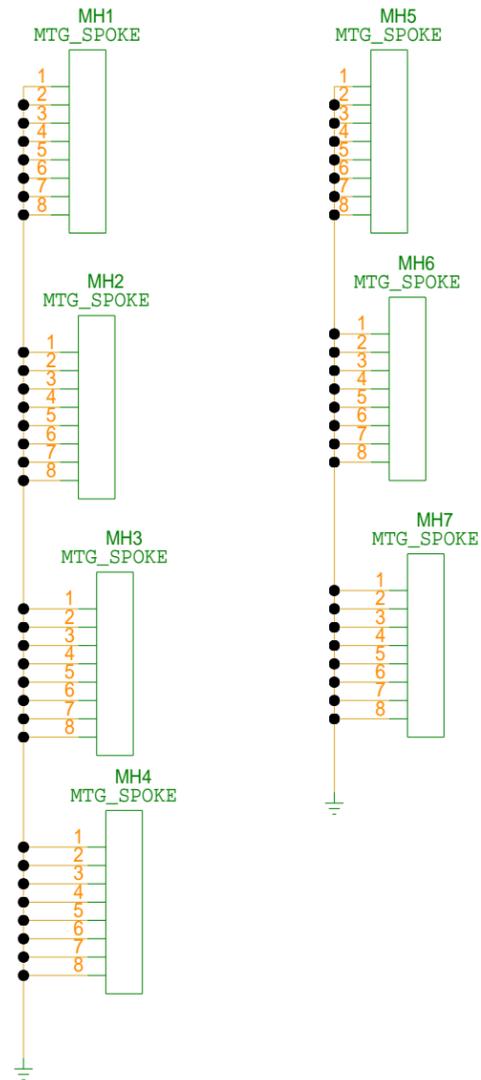


PCIE GEN 3 TEST CONNECTOR

DRAWING
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 ABBREV=PCIE_GEN3_CONN
 LAST_MODIFIED=Fri Oct 07 16:14:48 2011

Fri Oct 07 16:14:48 2011

TRL CALIBRATION STRUCTURES - SMA BASE BOARD BOTTOM SIDE



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Fri Oct 07 16:14:49 2011

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RISER CARD SCHEMATIC

RISER CARD

G45492-301 PBA,RC,PCIE GEN3 RC,PCIE G3 CEB RC,FAB3
 G45491-003 PB,EMSD,PCIE G3 CEB RC,FAB3 (NEW)
 G45487 PB,DOC,EMSD,PCIE G3,FAB3 (NEW)
 G47418 PB, SCHEMATIC, EMSD, PCIE G3, FAB 3

Wed Oct 05 16:49:52 2011

RISER CARD

DEPARTMENT
ESE

Intel Corporation
 2200 Mission College Blvd.
 P.O. BOX 58119
 Santa Clara, CA 95052-8119

SIZE
B

CODE
34649

DOCUMENT NUMBER
G47418

REV
0.1

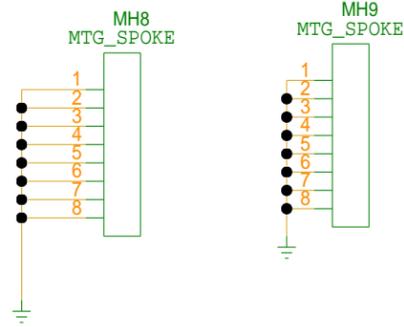
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SHEET 5 OF 6

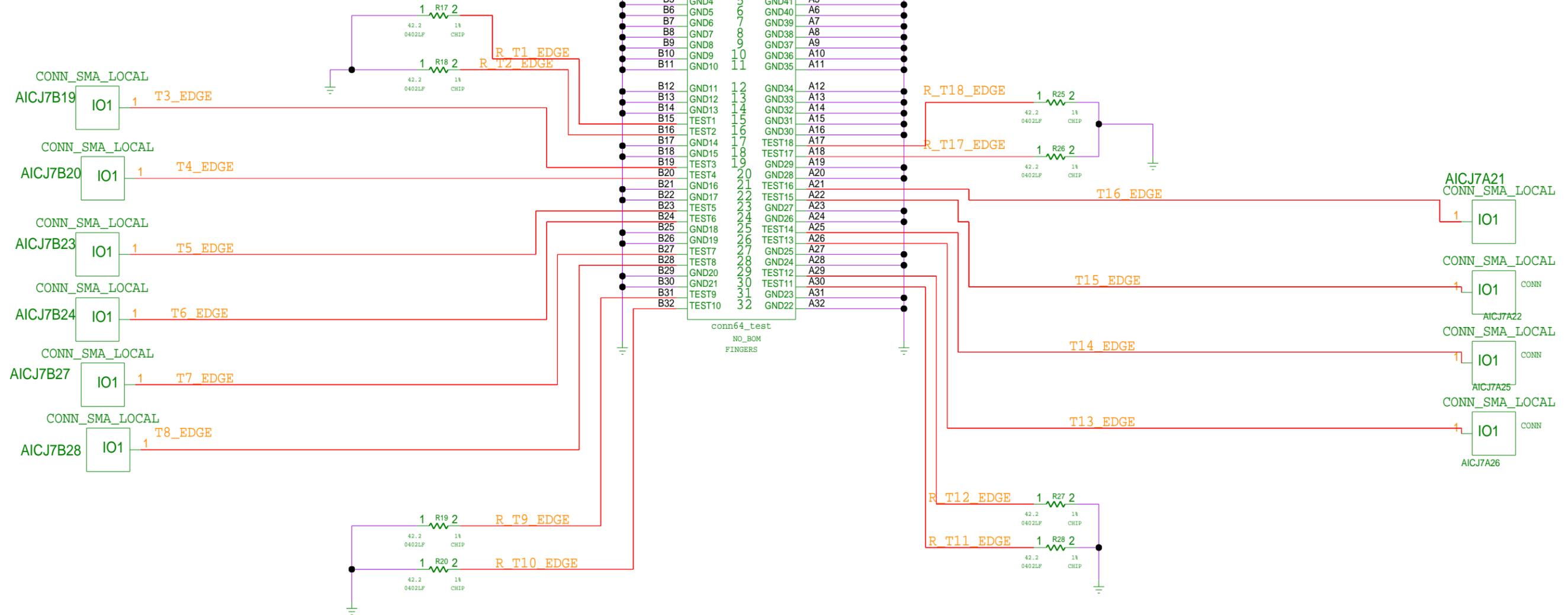
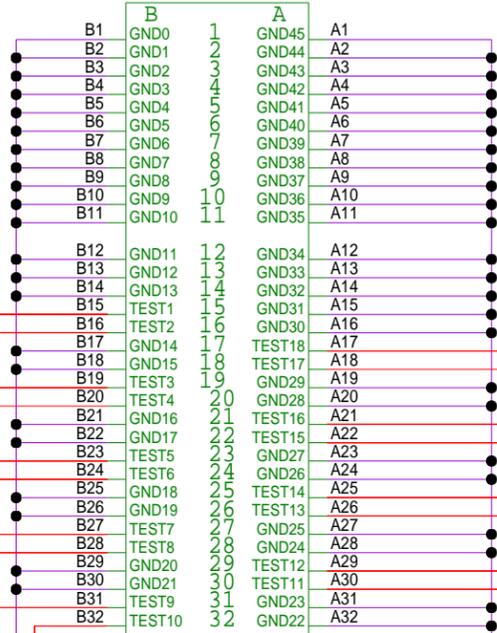
RISER CARD

BASELINE CASE
SMALL PAD WIDTH
LARGE PAD HEIGHT



PCIE EDGE FINGERS

J7



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